Docket No.: 42P18964

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of:

SHRIRAM RAMANATHAN, ET AL.

Application No.: 10/809,560

Filed: March 24, 2004

For: METHOD OF FORMING CHANNELS

ON AN INTEGRATED CIRCUIT DIE

AND DIE COOLING SYSTEMS INCLUDING SUCH CHANNELS

Examiner: Not yet Assigned

Art Group: Not yet Assigned

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY/SOKOLOFF, TAYLOR & ZAFMAN LLP

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Date: August 05, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Tamara M. Simpson

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number 10/809,560

Filing Date March 24, 2004

First Named Inventor Shriram Ramanathan

Art Unit Not yet Assigned

Examiner Name Not yet Assigned

Attorney Docket Number 42P18964

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NON PATENT LITERATURE DOCUMENTS			
Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
1	YOUNGCHEOL JOO ET AL, Fabrication of Monolithic Microchannels For IC Chip Cooling, 1995 IEEE, Pgs 362-367		
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Examiner	Date	
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